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INFORMATION DISCLOSURE

STATEMENT BY APPLICANT
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of 8

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Application Number	10/618,462				
Filing Date	07/11/2003				
First Named Inventor	Guangming Yin				
Art Unit	2817				
Examiner Name	Patricia T. Nguyen				
Attorney Docket Number	BP1817CON1				

			U. S. PATEN	DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2 (if known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		<sup>US-</sup> 4,545,023	10-01-1985	Mizzi	
		<sup>US-</sup> 4,680,787	07-14-1987	Marry	
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Examiner Cite			Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages		
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Translation is attached.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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of 8

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Art Unit	2817			
Examiner Name	Patricia T. Nguyen			
Attorney Docket Number	BP1817CON1			

			U. S. PATENT	DOCUMENTS	
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Complete if Known Substitute for form 1449/PTO **Application Number** 10/618,462 Filing Date 07/11/2003 INFORMATION DISCLOSURE First Named Inventor Guangming Yin STATEMENT BY APPLICANT Art Unit 2817 (Use as many sheets as necessary) **Examiner Name** Patricia T. Nguyen

Sheet	Sheet 3 of 8		Attorney Docket Number BP1817CON1		
			II S DATENT	r DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number  Number-Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
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## Complete if Known Application Number 10/618,462 Filing Date 07/11/2003 First Named Inventor Guangming Yin Art Unit 2817 Examiner Name Patricia T. Nguyen

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Examiner Name Patricia T. Nguyen

Sheet 4 of 8 Attorney Docket Number BP1817CON1

				DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2 (if known)</sup>	00.00.4004	Cills and and all	rigures Appear
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		<sup>US-</sup> 5,331,509	07-19-1994	Kikinis	
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Examiner	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
Initials*	No. <sup>1</sup>	Number-Kind Code <sup>2 (if known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
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		<sup>US-</sup> 5,680,633	10-21-1997	Koenck et al	
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(Use as many sheets as necessary)	Examiner Name	Patricia T. Nguyen	
Sheet 6 of 8	Attorney Docket Number	BP1817CON1	

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Cabonita				Application Number	10/618,462	
INF	ORMATION	DIS	CLOSURE	Filing Date	07/11/2003	
STA	STATEMENT BY APPLICANT			First Named Inventor	Guangming Yin	
	(Use as many she	ets as n	ecessary)	Art Unit	2817	
	(OSC us many sinc	- C13 d3 /		Examiner Name	Patricia T. Nguyen	
Sheet	7	of	8	Attorney Docket Number	BP1817CON1	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Bida, A Hybrid Wireless MAC Protocol Supporting Asynchronous and Synchronous MSDU Delivery Services, IEEE 802.11/91-92, September 1991, pages 1-46	
		Robert Meier's Master's Thesis, Mobile Computer Network Architecture, (May 1993), 82 pages	
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		Bagby, One Approach to Wireless Network Architecture, IEEE P802.11/91-2, January 1991, 1-15, 17-21	
		Biba, A Modest Proposal for a Asynchronous, Data Intensive, Wireless Local Area Network, IEEE P802.11/91-25, March 1991; pages 1-25	

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				Filing Date	07/11/2003 Guangming Yin		
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Sheet	8	of	8	Attorney Docket Number	BP1817CON1		

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Examiner Initials*	The state of the s		
		Heimschi et al; Merged CMOS/Bipolar Current Switch Logic (MCSL), IEEE Journal of Solid State Circuits, Vol.24, pp. 1307-11, October 1989, page 1307-1311	
		Elrabaa et al; Optimization of Digital BiCMOS Circuits, An Overview, IEEE 1992, 5 pages	
		Mudd et al, Very High Speed ECL/CMG Gate Arrays with Submicron Structures; IEEE 1989, 5 pages	

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